## 4 PRINTED CIRCUIT BOARDS

The printed circuit boards are factory-set for operation. Usually, you do not need to set or adjust them. This chapter describes the standard settings and adjustment required if a defective printed circuit board is replaced. It also describes the test pins and the LED indications.

The control unit printed circuit board includes the main unit printed circuit board and one or more cards or modules installed horizontally to the main-unit printed-circuit board.

These PC boards have interface connectors, LED indicators, and a plastic panel at the front. At the rear, there is a backplane connector.

## **4.1** MAIN BOARD (A16B-3200-0600, -0601)

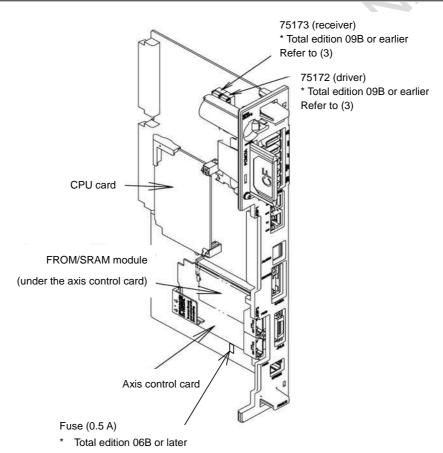


Fig.4.1 Main board

Name	Ordering Specification	<b>Board Specification</b>	Note
Main board	A05B-2500-H001	A16B-3200-0600	
	A05B-2500-H002	A16B-3200-0601	With force sensor I/F
CPU card	A05B-2500-H020	A20B-3400-0020	SDRAM 32M
	A05B-2500-H021	A20B-3400-0021	SDRAM 64M

Name	Ordering Specification	<b>Board Specification</b>	Note
Axis control card	A05B-2500-H045	A20B-3300-0445	4-axis
		A20B-3300-0785	
	A05B-2500-H040	A20B-3300-0448	8-axis
		A20B-3300-0788	
	A05B-2500-H041	A20B-3300-0447	12-axis
		A20B-3300-0787	
	A05B-2500-H042	A20B-3300-0442	16-axis
		A20B-3300-0782	
FROM/SRAM module	A05B-2500-H060	A20B-3900-0163	FROM 32M/ SRAM 1M
		A20B-3900-0223	
		A20B-3900-0283	
		A20B-3900-0297	
	A05B-2500-H061	A20B-3900-0164	FROM 32M/ SRAM 2M
		A20B-3900-0224	
		A20B-3900-0284	
		A20B-3900-0298	
	A05B-2500-H062	A20B-3900-0165	FROM 32M/ SRAM 3M
		A20B-3900-0225	
		A20B-3900-0285	
		A20B-3900-0299	
	A05B-2500-H063	A20B-3900-0166	FROM 64M/ SRAM 1M
		A20B-3900-0226	
		A20B-3900-0286	
	A05B-2500-H064	A20B-3900-0167	FROM 64M/ SRAM 2M
		A20B-3900-0227	
		A20B-3900-0287	
	A05B-2500-H065	A20B-3900-0228	FROM 64M/ SRAM 3M
		A20B-3900-0288	

## (1) Test pins

Name		Use
GND1		
GND2	,	
GND3	For testi	ng the printed circuit board
CACHOFF		
LOAD		

## (2) LEDs

Seven segment LED	Description
<b>B</b> .	A parity alarm occurred in a DRAM of the CPU card on the main board.
<b>a</b> .	A parity alarm occurred in a SRAM of the FROM/SRAM module on the main board.
<b>2.</b>	Bus error occurred on the communication controller.